

Claim 1 (currently amended). A power divider for a microwave waveguide having an input and multiple outputs, the divider comprising an impedance post, said impedance post being located in the waveguide between the input and at least two outputs,

at least one power selective probe, said at least one power selective probe(s) probe being respectively located in the waveguide between said impedance post and one of said at least two outputs,

and adjustment means to selectively set said at least one power selective probe(s) probe so as to alter the power through its respective output.

Claim 2 (cancelled).

Claim 3 (cancelled).

Claim 4 (currently amended). A power divider of claim 1 wherein said at least one power selective probe is variably altered by an adjustment means.

Claim 5 (cancelled).

Claim 6 (currently amended). A power divider of claim 4 characterized by said alterations being preset by physical replacement of said at least one power selective probe.

Claim 7 (original). A power divider of claim 1 characterized by two outputs.

Claim 8 (currently amended). A power divider of claim 1 characterized by the distance between said impedance post and said at least one power selective probe being within 0.1 of 91% of the wavelength in the waveguide at the center frequency of the operating bandwidth.

Claim 9 (currently amended). A power divider of claim 8 further characterized by the distance between said impedance post and said at least one power selective probe is 91% of the wavelength in the waveguide at the center frequency of the operating bandwidth.

Claim 10 (original). A power divider of claim 1 characterized by said impedance post having a diameter, said diameter being 4.4% of the wavelength in the waveguide at the center frequency of the operating bandwidth.

Claim 11 (cancelled).

Claim 12 (cancelled).

Claim 13 (original). A power divider for a microwave waveguide having an input and two outputs,

the divider comprising an impedance post, said impedance post being located in the waveguide between the input and at both outputs,

a first power selective probe, said first power selective probe being respectively located in the waveguide between said impedance post and a first of the outputs,

adjustment means to selectively set said first power selective probe so as to alter the power through its respective first output,

a second power selective probe, said second power selective probe being respectively located in the waveguide between said impedance post and a second of the outputs,

and selective means to selectively set said second power selective probe so as to alter the power through its respective second output.

Claim 14 (cancelled).

Claim 15 (original). A power divider of claim 1 characterized by each of said first and second power selective probes each comprising a capacitive probe, said capacitive probe being flanked by a pair of inductive members,

and each pair of said inductive members extending across the lateral cross section located on either side of said capacitive probe.

Claim 16 (original). A power divider of claim 1 characterized by the distance between said impedance post and each of said first and said second power selective probes being within 0.1 of 91% of the wavelength in the waveguide at the center frequency of the operating bandwidth.

Claim 17 (original). A power divider of claim 13 characterized by said impedance post having a diameter, said diameter being 4.4% of the wavelength in the waveguide at the center frequency of the operating bandwidth.

Claim 18 (original). A power divider of claim 1 characterized in that at least one of said power selective probes is located at least 1.5 wavelength in the waveguide within 0.1% of the center frequency of the operating bandwidth

from any component located along the longitudinal axis of the waveguide.

Claim 19 (original). A power divider for a microwave waveguide having an input and multiple outputs,

the divider comprising an impedance post, said impedance post being located in the waveguide between the input and at least two outputs,

a first power selective capacitive probe, said first power selective capacitive probe being respectively located in the waveguide between said impedance post and a first of said outputs,

selective means to selectively set said first power selective capacitive probe so as to alter the power through its respective first output,

said first capacitive probe being flanked by a first pair of inductive members,

each first pair of said inductive members extending across the lateral cross section located on either side of said first capacitive probe,

a second power selective capacitive probe, said second power selective capacitive probe being respectively located in the waveguide between said impedance post and a second of said the outputs,

selective means to selectively set said second power selective capacitive probe so as to alter the power through its respective second output,

said second capacitive probe being flanked by a second pair of inductive members,

and each second pair of said inductive members extending across the lateral cross section located on either side of said second capacitive probe.

claim 20 (cancelled).

Claim 21 (cancelled).

Claim 22 (cancelled).

Claim 23 (cancelled).